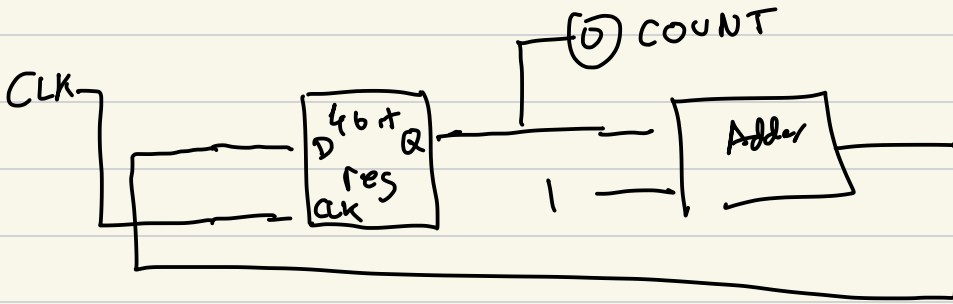


CS 315-01 Lab Latches FlipFlops Registers



SR Latch

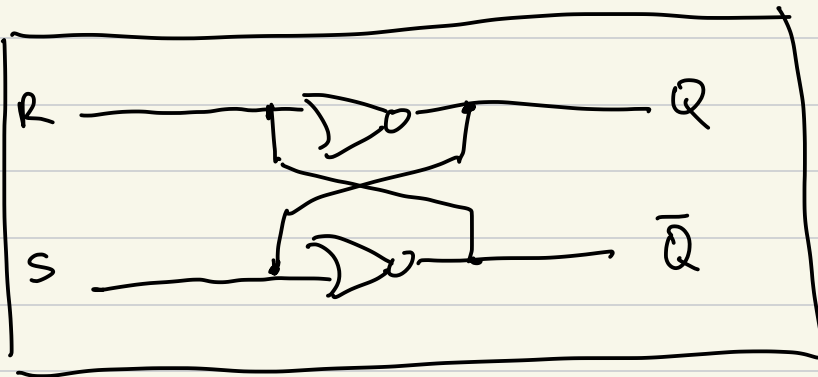
D Latch (clock)

D FlipFlop

1 bit register

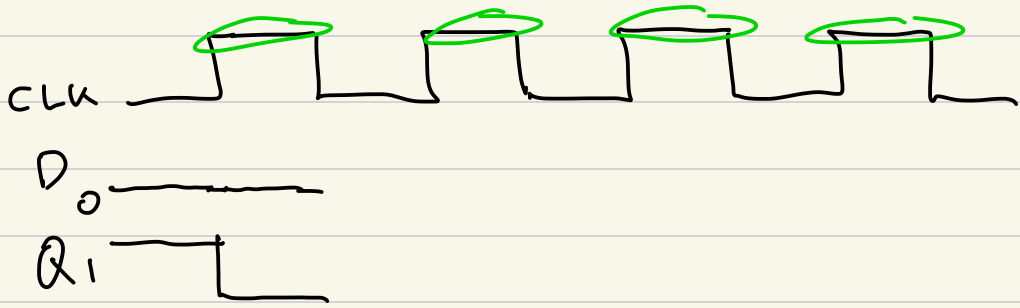
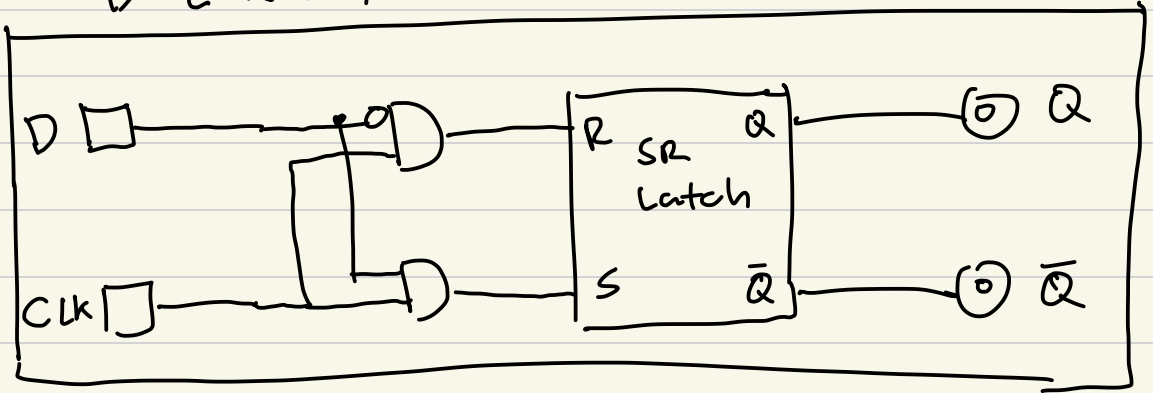
4 bit register

SR Latch

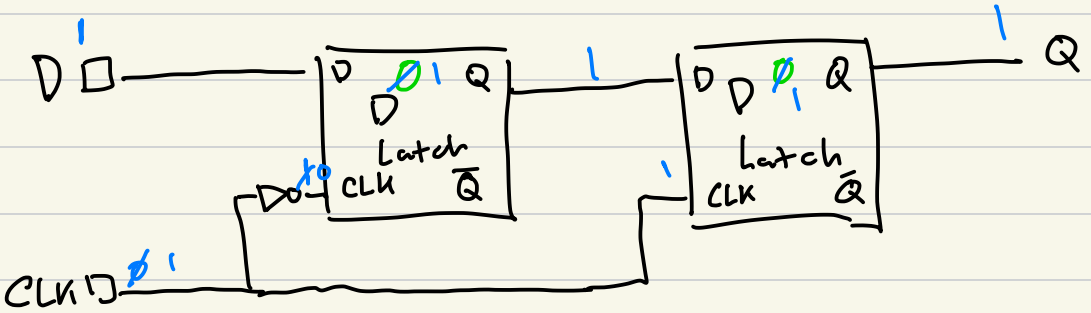


R	S	Q	\bar{Q}
1	0	0	1
0	0	0	1
0	1	1	0
0	0	1	0
1	1	X	X

D Latch



D FlipFlop

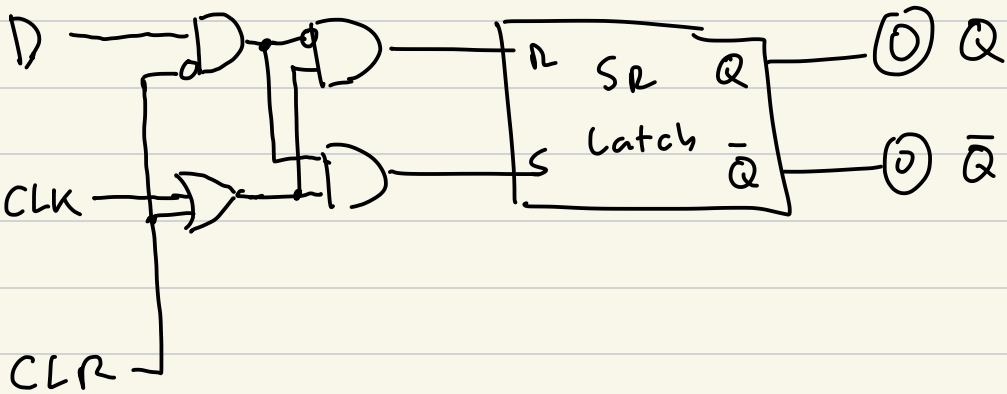


1 bit Register

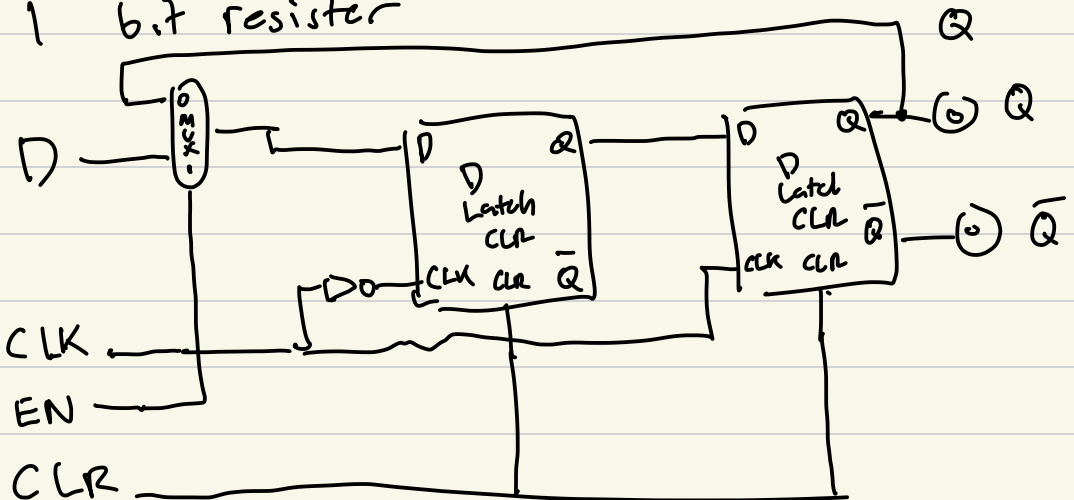
CLR Clear / reset

EN Enable

D Latch with CLR



1 bit register



N bit Register

